

UNITED STATES DISTRICT COURT
WESTERN DISTRICT OF WASHINGTON
AT SEATTLE

NXP USA, INC., and NXP B.V.,

Plaintiffs,

v.

IMPINJ, INC.,

Defendant.

CASE NO. 2:20-cv-01503-JHC

CLAIM CONSTRUCTION ORDER

I.

INTRODUCTION

Plaintiffs NXP USA, Inc. and NXP B.V. (collectively, “NXP”) brought this patent-infringement action against Defendant Impinj, Inc., alleging infringement of a group of patents relating to radio frequency identification (“RFID”) technology. The parties have asked the Court to construe certain claim terms within those patents. The Court hereby enters this order.

II.

BACKGROUND

A. Technology Background

RFID is a type of contactless wireless communication that uses electromagnetic frequencies to transmit identification information. *See generally* Dkt. # 220 (technology tutorial). RFID systems are used, for example, in retail stores for tracking merchandise and in warehouses for tracking inventory. *Id.* at 6. They are even used in U.S. passports and in various medical technologies. *Id.* RFID systems typically include a “data carrier” (or “tag”), which can attach to an object, and a “communication station” (or “reader”), which receives information from the data carrier through radio waves. The data carrier wirelessly transmits data to the communication station, which the reader uses to identify the object. *Id.* at 7. The data carrier often contains circuitry and a transmission means like an antenna. *Id.* at 8. Data carriers also typically have some form of memory that stores information. *Id.* And while some data carriers rely on a battery or other independence source for power, other tags are powered exclusively by the radio waves transmitted from the communication station. *Id.* at 9.

B. The Patents

NXP alleged in its complaint that Impinj infringed eight patents related to RFID technology. *See* Dkt. # 1. The parties later narrowed their dispute to six patents. *See* Dkt. # 176 at 2 & n.1.

The parties refer to three of these patents as the “wafer patents.” The three wafer patents are United States Patent Numbers 7,456,489 (“the ’489 Patent”), 7,538,444 (“the ’444 Patent”) and 8,415,769 (“the ’769 Patent”). These patents are directed to semiconductor “wafers” on which integrated circuits are formed.

1 The parties refer to the remaining three patents as the “circuit patents.” The three circuit
2 patents are United States Patent Numbers 7,257,092 (“the ’092 Patent”), 7,347,097 (“the ’097
3 Patent”), and 7,795,951 (“the ’951 Patent”). These patents are generally directed to components
4 or elements within the integrated circuits themselves.

5 In accordance with Local Patent Rule 132(c), the parties submitted a list of ten terms for
6 claim construction. Dkt. # 128. Impinj later moved for leave to brief five additional claim
7 terms. Dkt. # 146. The Court granted that motion in part, providing leave to brief three
8 additional claim terms. Dkt. # 186. These 13 terms for construction appear in the three circuit
9 patents (the ’092 Patent, ’097 Patent, and ’951 Patent) and one wafer patent (the ’769 Patent).

10 But of the 13 terms identified and briefed by the parties, only 9 are still at issue. First, the
11 Court granted Impinj’s motion for partial summary judgment as to the three wafer patents (the
12 ’489 Patent, the ’444 Patent, and the ’769 Patent). *See* Dkt. # 87 (Impinj’s partial summary
13 judgment motion); Dkt. # 242 (sealed order granting the motion). Therefore, the Court need not
14 construe any terms from the wafer patents, including the three terms identified in the ’769
15 Patent.

16 Second, the parties have informed the Court that one term relating to the ’092 Patent—
17 “processing means (10, 11) for processing specific useful data ($n \times \text{UDB}$)”—is no longer in
18 dispute. Impinj’s supplemental brief states that “Impinj hereby agrees to adopt NXP’s proposed
19 construction” and that “this term is no longer in dispute.” *See* Dkt. # 191 at 12.

20 Therefore, the Court must construe nine terms that appear in the ’092 Patent, the ’097
21 Patent, and the ’951 Patent.

22 1. The ’092 Patent

23 The ’092 patent describes techniques for communicating between a “communication
24 station” and a “data carrier.” ’092 Patent at 1:5–8. Prior art methods used a two-step process for

1 communication between the data carrier and the communication station. The communication
2 station would first conduct an “inventorization procedure” during which the communication
3 station would identify all the data carriers within its range. *Id.* at 1:10–38. After the
4 inventorization procedure, the data carrier would transmit “useful data” to the communication
5 station upon request. *Id.* at 1:38–47. The “disadvantage” of this two-step method was that “it
6 [took] a relatively long time” for the “useful data” to become usable by the communication
7 station. *Id.* at 1:42–44.

8 The ’092 Patent claims to improve upon the prior art by using an inventorization
9 procedure in which the “identification data block” and the “useful data” are transmitted
10 simultaneously. *Id.* at 11:7–17 (“[T]he invention is distinguished in that not only are parts of the
11 identification data blocks IDB transmitted into the communication station 1 in the course of
12 carrying out an inventorization procedure, but that during the inventorization procedure the
13 specific useful data n×UDB desired and/or required in the communication station 1 are also
14 simultaneously transmitted.”); Dkt. # 135 at 14. This simultaneous transmission shortens the
15 time it takes for the communication station to obtain the “useful data” stored in the data carriers.
16 ’092 Patent, 3:51–59.

17 2. The ’097 Patent

18 Many data carriers contain storage systems used to store information temporarily. These
19 carriers can, for example, temporarily store an indication of successful communication with a
20 communication station. Dkt. # 137 at 21. The information is stored and represented “by a value
21 of an information voltage that arises at the capacitor.” ’097 Patent, 1:45–47. The ’097 Patent
22 identifies a problem in the prior art: The information voltage would continuously decline due to
23 “unavoidable leakage currents in the circuit.” *Id.* at 1:62–2:1. This decline in voltage would
24

1 lead to an “unsatisfactory situation” because the information was “no longer able to be evaluated
2 after only a short period of time.” *Id.* at 2:2–7.

3 The ’097 Patent describes an invention that purports to solve this problem. The invention
4 described by the ’097 Patent provides “a substantially longer period of time during which the
5 stored information can be ascertained with high reliability.” *Id.* at 2:34–36. This also allows the
6 information to remain accessible if a brief supply-voltage failure occurs. *Id.* at 2:35–42. The
7 patent achieves this in part by adding a “voltage-raising means” to the “information-voltage
8 generating means.” *Id.* at 2:13–23; *see also* Dkt. # 135 at 17.

9 The specification describes the invention in more detail. It states that the carrier first
10 receives a wireless signal, which is used to form a supply voltage. ’097 Patent, 3:27–35. The
11 circuit produces a “control signal CS” that is “at most equal to the value of the supply voltage.”
12 *Id.* at 3:62–63. The circuit contains “information-voltage generating means” that receive the
13 control signal CS and uses the control signal CS to produce an “information voltage UI.” *Id.* at
14 3:63–66. The information-voltage generating means further consist of “voltage-raising means,”
15 “voltage-limiting means,” and a “charging-current generating stage.” *Id.* at 4:15–29.

16 3. The ’951 Patent

17 Some circuits found within RFID tags operate with relatively little power because they
18 receive power only through the wireless signal transmitted to them (known as the “supply
19 voltage”). *See* Dkt. # 220 at 13. Some of the circuit components, however, benefit from higher
20 voltages, while others benefit from lower voltages. *Id.*; ’951 Patent, 1:10–24. Therefore, many
21 circuits contain “voltage multiplier circuits” to modify the voltage. Dkt. # 220 at 13. The
22 purpose of the voltage multiplier is to increase or decrease a supply voltage to a desired level.
23 Dkt. # 149 at 13.

1 The '951 Patent describes a particular voltage multiplier circuit that “advantageously”
 2 enables multiplication of the voltage to a range of different voltages, including voltages both
 3 above and below the supply voltage. '951 Patent, 1:11–24, 1:49–63. The invention “control[s]”
 4 the generated voltage amplitude, producing a “regulated” voltage as it moves through the circuit
 5 components. *Id.* at 2:2–6.

6 As described in the specification, the voltage multiplier identified in the '951 Patent has
 7 two or more “multiplier stages.” *Id.* at 2:16–17. The voltage multiplier also includes a
 8 “feedback bias control circuit” that is coupled to receive the output of the voltage multiplier
 9 stages. *Id.* at 4:17–44, 6:1–33, Abstract. The feedback bias control circuit helps control or
 10 regulate the actions of the voltage multiplier by providing a “feedback bias control signal,”
 11 which is then fed to “regulated clocks” and to an “input level regulator.” *Id.* at 3:60–61
 12 (“regulated clock” with “an input for receiving a feedback bias control signal”), 3:23 (“input
 13 level regulator” with “a feedback bias control input”). The regulated clocks then provide a signal
 14 to each multiplier stage. *Id.* at 2:40–43, 3:46–55, 3:59–4:1. The input level regulator outputs a
 15 signal to the first multiplier stage. *Id.* at 3:22–32. The input level regulator “advantageously
 16 enables regulation of a voltage to a range of voltage levels” including voltages equal to or lower
 17 than the supply voltage. *Id.* at 3:24–28.

18 C. Procedural History

19 The parties filed initial claim construction briefs in September 2021. Dkt. ## 135, 149
 20 (Impinj’s briefs); Dkt. ## 137, 150 (NXP’s briefs). This case was reassigned to the undersigned
 21 judge in May 2022. Dkt. # 175. The parties filed supplemental claim construction briefs during
 22 the summer of 2022 addressing three additional terms in the '092 Patent.¹ Dkt. # 191 (Impinj’s
 23

24 ¹ As discussed above, the parties no longer dispute one of the terms in the '092 Patent.

supplemental brief); Dkt. # 195 (NXP’s supplemental brief). The parties argued their proposed claim constructions at a *Markman* hearing on October 4, 2022. Dkt. # 237. On October 20, 2022, the Court granted Impinj’s motion for partial summary judgment as to the three wafer patents, eliminating the need for construction of three terms in the ’769 Patent. *See* Dkt. # 87 (Impinj’s partial summary judgment motion); Dkt. # 242 (sealed order granting the motion).

III.

LEGAL PRINCIPLES

A. General Claim Construction Principles

“It is a ‘bedrock principle’ of patent law that ‘the claims of a patent define the invention to which the patentee is entitled the right to exclude.’” *Phillips v. AWH Corp.*, 415 F.3d 1303, 1312 (Fed. Cir. 2005) (en banc) (quoting *Innova/Pure Water, Inc. v. Safari Water Filtration Sys., Inc.*, 381 F.3d 1111, 1115 (Fed. Cir. 2004)); *see also Vitronics Corp. v. Conceptronic, Inc.*, 90 F.3d 1576, 1582 (Fed. Cir. 1996) (“[W]e look to the words of the claims themselves . . . to define the scope of the patented invention.”).

When construing a patent claim, the words of the claim “are generally given their ordinary and customary meaning.” *Phillips*, 415 F.3d at 1312 (quoting *Vitronics*, 90 F.3d at 1582). The “ordinary and customary meaning” of a term is the meaning of the words as understood by a person of ordinary skill in the art (“POSITA”) at the time of the invention. *Id.* at 1313. Although words in a claim are generally given their ordinary meaning, the Federal Circuit has recognized “two exceptions to this general rule: 1) when a patentee sets out a definition and acts as his own lexicographer, or 2) when the patentee disavows the full scope of a claim term either in the specification or during prosecution.” *Thorner v. Sony Comput. Ent. Am. LLC*, 669 F.3d 1362, 1365 (Fed. Cir. 2012). For the patentee’s unique definition to govern, the patentee must “clearly set forth a definition of the disputed claim term other than its plain and ordinary

1 meaning. *Id.* (internal quotation marks and citation omitted). The “standard for disavowal of
2 claim scope is similarly exacting.” *Id.* at 1366. “Absent a clear disavowal in the specification or
3 the prosecution history, the patentee is entitled to the full scope of its claim language.” *Home*
4 *Diagnostics, Inc. v. LifeScan, Inc.*, 381 F.3d 1352, 1358 (Fed.Cir.2004).

5 To determine the meaning of a disputed term, courts mainly rely on “intrinsic” evidence:
6 the claim language, the written description in the specification, and the patent’s prosecution
7 history. *Phillips*, 415 F.3d at 1311–17. A court’s analysis begins with the language in the
8 claims. *See id.* at 1314; *Innova/Pure Water*, 381 F.3d at 1116 (“[C]laim construction analysis
9 must begin and remain centered on the claim language itself.”). But claim terms are not to be
10 read in a vacuum. Rather, claims “are part of a fully integrated written instrument . . . consisting
11 principally of a specification that concludes with the claims.” *Phillips*, 415 F.3d at 1315 (citation
12 and internal quotation marks omitted); *see also id.* at 1313. The specification is particularly
13 important to claim construction and is often “the single best guide to the meaning of a disputed
14 term.” *Id.* at 1315 (quoting *Vitronics*, 90 F.3d at 1582). This is because the specification “aids
15 in ascertaining the scope and meaning of the claims.” *Id.* (quoting *Standard Oil Co. v. Am.*
16 *Cyanamid Co.*, 774 F.2d 448, 452 (Fed. Cir. 1985)). Thus, courts “rely heavily” on the
17 specification. *Id.* at 1317.

18 Courts often struggle when using the specification to guide the claim construction
19 inquiry. *Id.* at 1323. On the one hand, courts rely on the specification to help determine the
20 meaning of a disputed term. *Id.* On the other hand, the Federal Circuit has repeatedly warned
21 that a court may not read limitations from the specification into the claim. *Id.*; *see also Laitram*
22 *Corp. v. NEC Corp.*, 163 F.3d 1342, 1347 (Fed. Cir. 1998) (noting that it is a “well-established
23 principle that a court may not import limitations from the written description into the claims”);
24 *SciMed Life Sys., Inc. v. Advanced Cardiovascular Sys., Inc.*, 242 F.3d 1337, 1340 (Fed. Cir.

2001) (observing that one of the “cardinal sins of patent law” is “reading a limitation from the written description into the claims”). The “distinction between using the specification to interpret the meaning of a claim and importing limitations from the specification into the claim can be a difficult one to apply in practice.” *Phillips*, 415 F.3d at 1323. But the Federal Circuit has explained that “the line between construing terms and importing limitations can be discerned with reasonable certainty and predictability if the court’s focus remains on understanding how a person of ordinary skill in the art would understand the claim terms.” *Id.*

In addition to considering intrinsic evidence (like the claim language and specification), courts may also rely on “extrinsic” evidence. Extrinsic evidence “consists of all evidence external to the patent and prosecution history, including expert and inventor testimony, dictionaries, and learned treatises.” *Markman v. Westview Instruments, Inc.*, 52 F.3d 967, 980 (Fed. Cir. 1995), *aff’d*, 517 U.S. 370, (1996) (citation omitted). While extrinsic evidence may be useful, it is generally given less weight than intrinsic evidence. *Phillips*, 415 F.3d at 1317.

The construction of a patent’s claims is a question of law to be decided by the court. *Teva Pharmaceuticals USA, Inc. v. Sandoz, Inc.*, 574 U.S. 318, 321 (2015); *Markman v. Westview Instruments, Inc.*, 517 U.S. 370, 390 (1996).

B. Principles for Means-Plus-Function Claims

Under 35 U.S.C. § 112, ¶ 6 (now codified at 35 U.S.C. § 112(f)),² claims may be drafted in a “means-plus-function” format in which the claim “recites a function to be performed rather than definite structure or materials for performing that function.” *Lockheed Martin Corp. v.*

² The America Invents Act (“AIA”) amended and reorganized section 112, moving 35 U.S.C. 112, ¶ 6 to 35 U.S.C. § 112(f). But the AIA did not substantively change the means-plus-function portion of the statute. Because the patents at issue were filed before the AIA took effect, this order refers to the pre-AIA version of Section 112. See *Williamson v. Citrix Online, LLC*, 792 F.3d 1339, 1343 n.2 (Fed. Cir. 2015) (en banc).

1 *Space Sys./Loral, Inc.*, 324 F.3d 1308, 1318 (Fed. Cir. 2003) (citing 35 U.S.C. § 112, ¶ 6). By
2 allowing inventors to draft claims in this manner,

3 Congress struck a balance in allowing patentees to express a claim limitation by
4 reciting a function to be performed rather than by reciting structure for performing
5 that function, while placing specific constraints on how such a limitation is to be
6 construed, namely, by restricting the scope of coverage to only the structure,
7 materials, or acts described in the specification as corresponding to the claimed
8 function and equivalents thereof.

9 *Williamson v. Citrix Online, LLC*, 792 F.3d 1339, 1347 (Fed. Cir. 2015) (en banc).

10 Interpreting a means-plus-function limitation is a multi-step process. A court must first
11 determine whether the term is drafted in a means-plus-function format such that § 112, ¶ 6
12 applies. *MTD Prod. Inc. v. Iancu*, 933 F.3d 1336, 1344 (Fed. Cir. 2019); *Williamson*, 792 F.3d
13 at 1348. The presence of the word “means” in a claim creates a rebuttable presumption that the
14 claim is governed by § 112, ¶ 6. *See Egenera, Inc. v. Cisco Sys., Inc.*, 972 F.3d 1367, 1372 (Fed.
15 Cir. 2020) (“We presume that claim terms with the word ‘means’ invoke § 112(f)”; *Williamson*,
16 792 F.3d at 1348. But courts must not reflexively “evaluate[] form over substance when
17 evaluating whether a claim limitation invokes § 112, para. 6.” *Williamson*, 792 F.3d at 1348.

18 The Federal Circuit has explained:

19 In making the assessment of whether the limitation in question is a means-plus-
20 function term subject to the strictures of § 112, para. 6, our cases have emphasized
21 that the essential inquiry is not merely the presence or absence of the word
22 “means” but whether the words of the claim are understood by persons of ordinary
23 skill in the art to have a sufficiently definite meaning as the name for structure.

24 *Id.* (citation omitted); *see also MTD Prods.*, 933 F.3d at 1344 (“As part of this step, we consider
whether the claim limitation connotes ‘sufficiently definite structure’ to a person of ordinary skill
in the art.”). “To determine whether a claim recites sufficient structure, it is sufficient if the
claim term is used in common parlance or by persons of skill in the pertinent art to designate
structure, even if the term covers a broad class of structures and even if the term identifies the

1 structures by their function.” *Skky, Inc. v. MindGeek, s.a.r.l.*, 859 F.3d 1014, 1019 (Fed. Cir.
2 2017) (citation and quotation marks omitted); *see also MTD Prods.*, 933 F.3d at 1344; *Dyfan,*
3 *LLC v. Target Corp.*, 28 F.4th 1360, 1365–66 (Fed. Cir. 2022).

4 Once a court has determined that § 112, ¶ 6 applies, the court must “identify the claimed
5 function.” *Williamson*, 792 F.3d at 1351. And once a court has identified the claimed function,
6 the court “must determine what structure, if any, disclosed in the specification corresponds to the
7 claimed function.” *Id.*; *see also MTD Prods.*, 933 F.3d at 1344. This is necessary because a
8 means-plus-function claim subject § 112, ¶ 6 covers only the corresponding structure(s)
9 disclosed in the specification along with any equivalents thereof.

10 “Structure disclosed in the specification qualifies as ‘corresponding structure’ if the
11 intrinsic evidence *clearly links or associates* that structure to the function recited in the claim.”
12 *Williamson*, 792 F.3d at 1352 (emphasis added) (citation omitted). “Even if the specification
13 discloses corresponding structure, the disclosure must be of ‘adequate’ corresponding structure
14 to achieve the claimed function.” *Id.* (citation omitted). “The inquiry is whether one of skill in
15 the art would understand the specification itself to disclose a structure, not simply whether that
16 person would be capable of implementing a structure.” *Biomedino, LLC v. Waters Techs. Corp.*,
17 490 F.3d 946, 951 (Fed. Cir. 2007).

18 But “[i]f the patentee fails to disclose adequate corresponding structure, the claim is
19 indefinite,” and is therefore invalid. *Williamson*, 792 F.3d at 1352. When “a person of ordinary
20 skill in the art would be unable to recognize the structure in the specification and associate it
21 with the corresponding function in the claim, a means-plus-function clause is indefinite.” *Id.*;
22 *see also Atmel Corp. v. Info. Storage Devices*, 198 F.3d 1374, 1382 (Fed. Cir. 1999) (The
23 structure “must be disclosed in the written description in such a manner that one skilled in the art
24 will know and understand what structure corresponds to the means limitation.”). If the patentee

1 “fails to set forth an adequate disclosure, the applicant has in effect failed to particularly point
2 out and distinctly claim the invention as required by [the statute].” *Noah Sys., Inc. v. Intuit Inc.*,
3 675 F.3d 1302, 1311–12 (Fed. Cir. 2012) (citation omitted). This renders the term indefinite. *Id.*

4 IV.

5 DISCUSSION

6 A. The '092 Patent

7 1. “characteristic identification data block (IDB)”

8 The '092 Patent describes an RFID system in which identification data and “useful data”
9 are simultaneously transmitted from the data carrier to the communication station. The patent
10 describes the identification data stored within the data carrier as the “characteristic identification
11 data block (IDB).” For example, claim 1 of the '092 Patent discusses a “data carrier” that
12 “comprises a characteristic identification block (IDB).” '092 Patent, 17:48–55. The term is used
13 in the same manner in independent claims 1, 6, 7, 11, 15, and 19, as well as several dependent
14 claims. The parties dispute the meaning of the term “characteristic identification data block
15 (IDB).”

16 Impinj construes the term to mean “a serial number.” Dkt. # 135 at 14. This, Impinj
17 says, is because the specification defines the term “characteristic identification data block (IDB)”
18 as a “serial number.” *See Phillips*, 415 F.3d at 1321 (“[T]he specification acts as a dictionary
19 when it expressly defines terms used in the claims or when it defines terms by implication.”
20 (internal quotation marks and citation omitted)); *id.* at 1316 (“[T]he specification may reveal a
21 special definition given to a claim term by the patentee that differs from the meaning it would
22 otherwise possess. In such cases, the inventor’s lexicography governs.”).

23 NXP construes the term to mean “identification data stored in memory,” or otherwise
24 asks the Court to construe the term according to its plain and ordinary meaning. Dkt. # 137 at

1 18. NXP argues that the term refers to any identification data stored in memory, whether that
2 data take the form of a serial number or something else. *Id.* at 18–19. NXP responds to Impinj’s
3 construction by observing that when the specification uses the term “serial number,” it does so
4 only to provide an example; the use of “serial number” in the specification was not intended to
5 limit the scope of the claim term. *Id.* at 19.

6 The Court agrees with NXP and adopts the construction of “identification data stored in
7 memory.” Neither the claim terms nor the specification limit “identification data block” to any
8 particular format like a serial number. When broken down, the term refers to (1) a block (2) of
9 data (3) used for identification, without limiting the format of that data block. This is the
10 “ordinary and customary meaning” of the words to a POSITA.

11 Impinj responds that the specification “defines” “identification data block” as a serial
12 number. Dkt. # 135 at 14–15. As evidence, Impinj points to a portion of the specification that
13 describes prior art in which

14 the first step is to carry out an inventorization procedure, which usually consists
15 of a plurality of procedure runs and in which so many procedure runs are carried
16 out until the identification data blocks stored in the data carriers, *also denoted*
serial numbers, of all data carriers present in a communication region of the
communication station are known in the communication station.

17 ’092 Patent, 1:22–29 (emphasis added). Impinj says that the patentee defined “identification
18 data block” as “a serial number” because the term “serial number” is preceded by the word
19 “denote,” which is often used to designate meaning or definition. Dkt. # 135 at 15 n.6.

20 To be sure, an inventor may “act[] as his own lexicographer” by clearly setting forth in
21 the specification a different definition of the claim term in question. *Thorner*, 669 F.3d at 1365.
22 When the inventor provides a “special definition” for a given claim term, “the inventor’s
23 lexicography governs.” *Phillips*, 415 F.3d at 1316.

1 But “a claim term is only given a special definition different from the term’s plain and
2 ordinary meaning if the ‘patentee . . . clearly set[s] forth a definition of the disputed claim term
3 other than its plain and ordinary meaning.’” *Akamai Techs., Inc. v. Limelight Networks, Inc.*, 805
4 F.3d 1368, 1375 (Fed. Cir. 2015) (alterations in original) (quoting *Thorner*, 669 F.3d at 1365).
5 While a definition provided in a specification need not be “express” or “explicit” to control the
6 scope of the term, it must still be clear. *See Phillips*, 415 F.3d at 1321. The patentee must
7 “clearly express an intent to redefine the term.” *Thorner*, 669 F.3d at 1365 (internal quotation
8 marks and citation omitted).

9 The language to which Impinj points is anything but a “clear[]” statement sufficient to
10 redefine the term. *Akamai*, 805 F.3d at 1375 (citation omitted). The patent references “serial
11 number” twice. “Identification data block” appears more than 130 times in the specification
12 alone. Dkt. # 244 at 52. It would be odd to focus on the two “serial number” references to
13 conclude that the patentee intended to restrict the scope of the disputed term.

14 This is particularly true when it is unclear whether the patent’s use of the word “denoted”
15 was meant to redefine the term “identification data block.” Even ignoring the awkward phrasing
16 of the passage, “denote” does not necessarily create an exclusive definition. Using the same
17 dictionary cited by Impinj, the word also means “to serve as an indication of,” suggesting that
18 “serial number” could be an example of (or indicative of) the identification data block.
19 “Denote,” *Merriam-Webster Dictionary*, <https://www.merriam-webster.com/dictionary/denote>.
20 Moreover, NXP observes that the ’092 Patent tends to use “i.e.” as a signal when defining a term.
21 *See, e.g.*, ’092 Patent, 1:32–33 (“inventorized, i.e. identified”). The Federal Circuit has declined
22 to find a statement to be “definitional” when it did not “accord with the linguistic formula used
23 by the patentee to signal” other definitions. *Med. Co. v. Mylan, Inc.*, 853 F.3d 1296, 1306 (Fed.
24 Cir. 2017).

1 Perhaps more importantly, Impinj’s proposed construction would conflict with other
2 portions of the specification. First, the other instance of the term “serial number” in the patent
3 undermines Impinj’s construction. The specification describes an embodiment in which
4 communication occurs using “the first four (4) bits of its identification data block IDB, thus by
5 its serial number.” ’092 Patent, 11:60–61. A plausible reading of this passage suggests that in
6 this embodiment, the patentee thought of the serial number as only the first four bits of the
7 identification data block, not the entire thing. And “where claims can reasonably [be] interpreted
8 to include a specific embodiment, it is incorrect to construe the claims to exclude that
9 embodiment, absent probative evidence on the contrary.” *GE Lighting Sols., LLC v. AgiLight,*
10 *Inc.*, 750 F.3d 1304, 1311 (Fed. Cir. 2014) (citation omitted).

11 Second, the patent discloses an embodiment in which “special data” that is “useful or
12 absolutely necessary for other applications” may be included within the identification data block.
13 ’092 Patent, 17:10–18. To be sure, the patent does not explain the meaning of “special data.”
14 But this embodiment would suggest to a POSITA that the phrase “identification data block”
15 encompasses identification data that may be broader than a mere serial number. *See GE*
16 *Lighting*, 750 F.3d at 1311.

17 Third, NXP points to industry materials that use a similar term—“Unique identifier
18 (UID)”—to include more than a serial number. The ISO/IEC15693-3 standard discloses a 64-bit
19 “Unique identifier (UID)” that encompasses a 48-bit “serial number,” an 8-bit manufacturer
20 code, and 8 bits of other data. Dkt. # 137 at 19. A POSITA that is familiar with this industry
21 standard would thus understand the similar term “identification data block” to refer to a broader
22 class of data than just a “serial number.”

23 Impinj’s proposed construction would impermissibly import limitations from the
24 specification into the claims. *See SciMed*, 242 F.3d at 1340. And because the patentee did not

1 clearly re-define the term “characteristic identification data block (IDB)” to mean “a serial
2 number,” the patentee is entitled to the full scope of the term’s ordinary meaning. Accordingly,
3 the Court construes the term “characteristic identification data block (IDB)” as “identification
4 data stored in memory.”

5 2. “specific useful data (n×UDB)”

6 Claim 1 of the ‘092 Patent describes a communication method by which “specific useful
7 data (n×UDB)” is transmitted from a data carrier to a communication station. *See, e.g.*, ‘092
8 Patent, 17:56–59. The term appears in nearly every claim and is used in the same way as in
9 claim 1. The parties dispute the term’s meaning.

10 As an initial matter, the parties agree that “specific useful data (n×UDB)” refers to a
11 subset of all available “useful data.” *See* Dkt. ## 137 at 20–21; 135 at 15; *see also* ‘092 Patent at
12 10:13–19. As Impinj states, “[t]he parties agree that the term means something less than all
13 useful data (‘some’ or a ‘portion of’).” Dkt. # 135 at 15. But the parties dispute whether the
14 term refers to any subset of useful data or whether the term is limited to a particular subset of
15 useful data. NXP argues that the term refers to any subset of useful data and proposes a
16 construction of “some, but not all, useful data (UD).” Dkt. # 137 at 20. Impinj argues that the
17 term is limited to a particular subset of useful data. Dkt. # 135 at 15. In particular, Impinj
18 construes the term to mean “a portion of useful data (UD), where the portion is specified in a
19 request from the communication station that indicates a useful data start block and a specific
20 number n of useful data blocks.” *Id.* The Court adopts NXP’s proposed construction and
21 construes the term “specific useful data (n×UDB)” to mean “some, but not all, useful data
22 (UD).” *Id.*

23 To start, nothing in the claim language suggests an intent to limit “specific useful data
24 (n×UDB)” to that which has been requested by the communication station. *See Williamson*, 792

1 F.3d at 1346 (“[I]t is the *claims*, not the written description, which define the scope of the patent
2 right.” (internal quotation marks and citation omitted)).

3 The Court next considers the specification to guide its construction of the term. *Phillips*,
4 415 F.3d at 1315 (noting that “the specification ‘is always highly relevant to the claim
5 construction analysis’” (quoting *Vitronics*, 90 F.3d at 1582)). The specification discloses at least
6 two embodiments relevant to the construction of this term. First, the specification primarily
7 focuses on an embodiment in which a data carrier transmits specific useful data n×UDB in
8 response to a “request” from the communication station. *See, e.g.*, ’092 Patent, 16:22–28.
9 Second, the specification discloses—albeit in a more cursory fashion—an embodiment³ in which
10 a data carrier “automatically” transmits “a specific selection of useful data blocks.” *Id.* at 16:22–
11 34. As to that second embodiment, the specification teaches that “it is also possible to select a
12 design in which no request for useful data is made by the communication station 1, but in which
13 . . . *automatically a specific selection of useful data blocks UDB are transmitted* from each data
14 carrier 2 (DC) to the communication station 1.” ’092 Patent at 16:22–34 (emphasis added).
15 Impinj’s proposed construction—which limits the term to specific useful data transmitted in
16 response to a *request*—would improperly exclude this second, “automatic” embodiment. *See GE*
17 *Lighting*, 750 F.3d at 1311 (“[W]here claims can reasonably [be] interpreted to include a specific
18 embodiment, it is incorrect to construe the claims to exclude that embodiment, absent probative
19 evidence on the contrary.” (citation omitted)).

20 Impinj’s main argument focuses on the parenthetical “(n×UDB),” which follows each
21 instance of the phrase “specific useful data” within the claims. *See* Dkt. ## 135 at 15–16; 149 at
22

23 ³ At the *Markman* hearing, the parties agreed that the ’092 Patent’s discussion of automatically
24 transmitted specific data constitutes an “embodiment” despite its cursory treatment in the specification.
Dkt. # 244 at 66, 69.

9. Impinj argues that NXP’s proposed construction reads the parenthetical out of the claim language, violating the principle that “[a] claim construction that gives meaning to all the terms of the claim is preferred over one that does not do so.” *Merck & Co. v. Teva Pharms. USA, Inc.*, 395 F.3d 1364, 1372 (Fed. Cir. 2005). According to Impinj, the specification defines “ $n \times \text{UDB}$ ” in a manner more consistent with its construction:

The *useful data start block* included in a request data block RDB and the number n of useful data blocks are detected by the block detection means 38, the result being that the block detection means 38 ensure that, beginning with the determined start block, a total of n useful data blocks UDB, i.e. $n \times \text{UDB}$ are read out as specific useful data

’092 Patent at 10:9–15 (emphasis added); Dkt. # 135 at 16. Impinj argues that “ $n \times \text{UDB}$ ”—“defined” using the phrase “i.e.”⁴ in a paragraph that discusses transmission based on a request—refers to specific useful data transmitted in response to a request, not specific useful data that is automatically transmitted. Moreover, Impinj notes that when the specification discusses automatic transmission of data, it omits the term “ $n \times \text{UDB}$.” See ’092 Patent, 16:28–33 (“[I]t is also possible to select a design in which no request for useful data is made by the communication station 1, but in which . . . automatically a specific selection of useful data blocks UDB are transmitted from each data carrier.” (emphasis added)). According to Impinj, this suggests that the inclusion of “($n \times \text{UDB}$)” in the claims refers only to specific useful data transmitted in response to a request.

But Impinj’s argument does not persuade the Court. First, it is unclear whether the patent defines “ $n \times \text{UDB}$ ” to refer only to requested data. While the phrase “i.e. $n \times \text{UDB}$ ” appears in a sentence that otherwise discusses data transmitted in response to a request, it does not necessarily follow that “i.e. $n \times \text{UDB}$ ” refers only to requested data. The pertinent portion of the sentence

⁴ NXP’s brief expressly states (though in the context of a different term) that the ’092 Patent uses “i.e.” to introduce a definition. Dkt. # 150 at 11.

1 states: “. . . a total of n useful data blocks UDB, i.e. n×UDB are read out as specific useful data
 2” ’092 Patent, 10:13–15. The most natural reading of the clause beginning with “i.e.
 3 n×UDB” is that it modifies the immediately antecedent phrase, “a total of n useful data blocks
 4 UDB.” Read in this way, “n×UDB” simply refers to a number (n) of “useful data blocks”
 5 (UDB), whether or not the “n” number of data blocks are transmitted automatically or in
 6 response to a request.

7 Second, “n×UDB” is used inconsistently throughout the ’092 Patent. While the term
 8 often accompanies the words “specific useful data” in the specification, it does not always do
 9 so.⁵ For example, while one sentence in the specification describes a method by which
 10 “transmission of specific useful data is carried out from the at least one data carrier,” ’092 Patent,
 11 1:67–2:1 (lacking reference to “n×UDB”), another nearly identical sentence in the specification
 12 includes “n×UDB,” stating that “the specific useful data (n×UDB) are transmitted from the at
 13 least one data carrier,” *id.* at 3:66–4:1. The parenthetical is even used inconsistently within a
 14 single paragraph that indisputably refers to the same “specific useful data.” *Id.* at 4:18–30
 15 (explaining that it is advantageous for the “specific useful data (n×UDB) [to be] transmitted in
 16 time after the data (NKP IDB) from the identification data block,” but later stating that “it is also
 17 possible for the specific useful data to be transmitted . . . before the data from the identification
 18 data block.”)

19 The Court can discern no logic for the sporadic inclusion or omission of the parenthetical.
 20 Given this inconsistent drafting, Impinj’s observation that the specification does not use
 21 “n×UDB” when describing automatic data transmission does not carry much explanatory power.
 22 The Court is skeptical that a POSITA reading the patent would place so much weight on the
 23

24 ⁵ While the term “n×UDB” appears inconsistently throughout the specification, the claims
 consistently use the parenthetical “(n×UDB)” after every use of the words “specific useful data.”

1 presence or absence of the parenthetical; a POSITA is more likely to think that “specific useful
2 data” refers to the same thing throughout the patent, whether or not it is succeeded by the
3 “(n×UDB)” parenthetical. And because neither the claim language nor the specification limit
4 “specific useful data” to any particular subset of data, the Court construes the term “specific
5 useful data (n×UDB)” to mean “some, but not all, useful data (UD).”

6 3. “output means (40, 34, 32, 25) for outputting to the communication station (1)
7 specific useful data (n×UDB)”

8 Claim 15 of the ‘092 Patent describes a data carrier that contains “output means (40, 34,
9 32, 25) for outputting to the communication station (1) specific useful data (n×UDB).” ‘092
10 Patent, 20:2–5. The reference numbers in parentheses—40, 34, 32, and 25—refer to elements
11 described in the specification. At various points, the specification describes “processing means
12 40,” “coding means 34,” “modulation means 32,” and “transmission means 25.” Dkt. # 195 at 8,
13 10–11.

14 The parties agree that the term “output means (40, 34, 32, 25) for outputting to the
15 communication station (1) specific useful data (n×UDB)” is a means-plus-function limitation
16 subject to § 112, ¶ 6. Dkt. ## 191 at 7; 195 at 10. The parties also agree the claimed function is
17 “outputting to the communication station specific useful data (n×UDB).” Dkt. ## 191 at 7; 195
18 at 10.

19 The parties dispute, however, the corresponding structure. NXP says that the
20 corresponding structure is “processing means 40, coding means 34, modulation means 32,
21 transmission means 25, and equivalents.” Dkt. # 195 at 10. NXP argues that the four
22 parenthetical reference numbers—40, 34, 32, and 25—guide the patent reader to the four
23 corresponding components in the specification bearing those same reference numbers, “clearly
24 link[ing]” the outputting function with those four components. *Williamson*, 792 F.3d at 1352.

1 Impinj argues that it is improper for the Court to consider the reference numbers when
2 construing the claim. Dkt. # 191 at 6–7. But if the Court does consider the reference numbers,
3 Impinj argues that the patent discloses no corresponding structure for “output means” and that
4 the claim therefore invalid for indefiniteness.⁶ *Id.* at 9–11. In the alternative, Impinj says that
5 the corresponding structure for “output means . . .” is an “antenna element” and its equivalents.
6 *Id.* at 8.

7 A claim that employs a means-plus-function form must disclose adequate “corresponding
8 structure” in the specification; if it does not, it is invalid as indefinite. *Williamson*, 792 F.3d at
9 1352. “Structure disclosed in the specification qualifies as ‘corresponding structure’ if the
10 intrinsic evidence clearly links or associates that structure to the function recited in the claim.”
11 *Id.* (citation omitted). “Even if the specification discloses corresponding structure, the disclosure
12 must be of ‘adequate’ corresponding structure to achieve the claimed function.” *Id.* (citation
13 omitted). “The inquiry is whether one of skill in the art would understand the specification itself
14 to disclose a structure, not simply whether that person would be capable of implementing a
15 structure.” *Biomedino*, 490 F.3d at 951.

16 The parties first dispute the role that reference numbers play in claim construction. This
17 issue is dispositive of this term. If the Court gives the reference numbers no weight, then
18 construction of this term is straightforward: The four reference numbers serve as the only
19 reasonably specific clues about what corresponding structures fulfill the outputting function.
20 The specification discusses “output means” only twice. Neither use of the term conveys
21 anything close to corresponding structure; each instance discusses “output means” in purely
22

23 ⁶ Impinj’s brief seems to focus only on the lack of structure disclosed for “modulation means”
24 and “coding means,” and does not focus on “transmission means” or “processing means.” *See* Dkt. # 191
at 9–11.

functional terms. '092 Patent, 3:5, 3:31. Nor does the specification “clearly link[]” any other corresponding structure to the outputting function. *Williamson*, 792 F.3d at 1352.

Addressing this apparent question of first impression, the Court concludes that the reference numbers in the claim cannot alone indicate “corresponding structure” for this means-plus-function claim. The general rule is that reference numbers used in the claims of a patent carry no meaning; they do not limit or alter the scope of the term in any way. The Manual of Patent Examination and Procedure states that “[g]enerally, the presence or absence of such reference characters does not affect the scope of a claim.” *See Manual of Patent Examination and Procedure* (“MPEP”), § 608.01(m) (9th ed., 2019).

And while the Federal Circuit does not appear to have addressed the question,⁷ district courts have come to the same conclusion. *See Core Wireless Licensing S.A.R.L. v. LG Elecs., Inc.*, No. 2:14-CV-0911-JRG-RSP, 2015 WL 6956722, at *7 (E.D. Tex. Nov. 9, 2015) (“Courts that have considered the implications of the use of reference numbers in a claim have followed the general rule that reference numbers do not limit the claims.”); *id.* (“[T]he rationale for excluding such numbers is equally applicable to means-plus-function terms.”); *Millipore Corp. v. W.L. Gore Assocs., Inc.*, No. CIV.A. 11-1453 ES, 2012 WL 5250386, at *4 (D.N.J. Oct. 24, 2012) (“[T]he parties agree that the reference characters have no special effect on claim scope.”); *KEG Kanalreinigungstechnik GmbH v. Laimer*, No. 1:11-CV-1948-JEC, 2013 WL 8719444, at *30 (N.D. Ga. Jan. 11, 2013), *report and recommendation adopted*, No. 1:11-CV-1948-JEC, 2013 WL 11904722 (N.D. Ga. Feb. 21, 2013) (observing that the MPEP “provides that reference

⁷ NXP cites several cases in which the Federal Circuit relied on reference numbers to determine claim meaning. *See Ironworks Patents LLC v. Samsung Elecs. Co.*, 798 Fed. App'x 621, 625–26 (Fed. Cir. 2020); *Superior Fireplace Co. v. Majestic Prods. Co.*, 270 F.3d 1358, 1374 (Fed. Cir. 2001); Dkt. # 195 at 6–7. But those cases involved reference numbers in the written description; neither involved reference numbers in the claims themselves.

1 numerals *may*, as an option, be used in claims, but doing so has *no effect on the scope of the*
2 *claims*” (second emphasis added)); *Relume Corp. v. Dialight Corp. et al.*, 63 F. Supp. 2d 788,
3 796, n.6 (E.D. Mich. 1999) (“A reference numeral is simply a convenient tool for directing the
4 reader to an example of the element the patentee has claimed. Had the drafter wanted to
5 incorporate the limitations of the preferred embodiment into the language of claim 1, he or she
6 could have done so quite easily with words.”).

7 NXP does not dispute this general rule. NXP instead argues that the reference numbers
8 in the claim at issue are not used to limit the claim or alter the claim scope. Dkt. # 195 at 6–7.
9 In the context of this means-plus-function claim, NXP says, the reference numbers merely guide
10 the reader to structures in the specification that qualify as corresponding structure; they do not
11 otherwise alter the scope of the term itself.

12 The Court recognizes that NXP presents a plausible argument. Means-plus-function
13 claims are unique in patent law: While most of patent law forbids the importation of limitations
14 from the specification into the claims, *see SciMed*, 242 F.3d at 1340, means-plus-function claims
15 require the court to look to the specification to determine the metes and bounds of the claim. A
16 means-plus-function claim is limited to the corresponding structure disclosed in the specification,
17 requiring a degree of interaction between the claims and the specification. This might justify an
18 exception to the general rule, allowing the patentee to conveniently refer back to the
19 specification and “clearly link[]” the claimed function to the corresponding structure using
20 reference numbers.

21 But the Court concludes that patent law would appear to be better served by a bright-line
22 rule: Reference numbers in claims do not affect the construction or scope of a claim. With a
23 bright-line rule, patentees are on notice that they cannot rely on reference numbers to convey
24

1 substantive meaning in the claims, whether those reference numbers are found in a means-plus-
2 function claim or not.

3 Carving out an exception to this general rule could create uncertainty and odd results. If
4 reference numbers in claims affect construction of means-plus-function claims but not other
5 claims, there would be two parallel sets of interpretative rules: one for means-plus-function
6 claims, and another for all other claims. *Cf. MTD Prods.*, 933 F.3d at 1342 (applying the same
7 interpretative canon—that claims must be interpreted in light of the written description—
8 regardless of whether those claims are drafted in means-plus-function form). This dichotomy is
9 particularly troublesome because it is often unclear whether a term *is*, in fact, a means-plus-
10 function claim. A patentee may not know *ex ante*, then, whether their inclusion of reference
11 numbers will be treated as entirely irrelevant or whether (as here) a court may one day interpret
12 them to carry dispositive meaning.

13 Moreover, a bifurcated interpretative rule could complicate the corresponding-structure
14 analysis. For example, it could be ambiguous whether the patentee intended for the list of
15 reference numbers to be exhaustive of all other potential corresponding structure. Even the
16 position of the reference numbers within a sentence could lead to interpretative challenges.
17 Thus, patent law would appear to be best served by a bright-line rule that gives reference
18 numbers in a claim no role in determining the substantive scope of the claim.

19 NXP argues that reliance on reference numbers to indicate corresponding structure is
20 different than using reference numbers to “limit” the scope of the claim. Instead, NXP says, a
21 reference number “is simply a convenient tool for directing the reader to an example of the
22 element the patentee has claimed.” *Relume*, 63 F. Supp. 2d at 796, n.6 (but ultimately
23 concluding that the reference numbers play no role in a claim’s scope). But when a court
24 determines the corresponding structure of a means-plus-function claim, the court is essentially

“limiting” the claim term. The court takes what is otherwise a broad, indefinite claim drafted in functional form and limits it in some fashion based on the structure disclosed in the specification. *Cf. Aristocrat Techs. Australia Pty Ltd. v. Int’l Game Tech.*, 521 F.3d 1328, 1333 (Fed. Cir. 2008) (“The point of the [corresponding-structure] requirement that the patentee disclose particular structure in the specification and that the *scope of the patent claims be limited to that structure* and its equivalents is to avoid pure functional claiming.” (emphasis added)); *Finisar Corp. v. DirecTV Grp., Inc.*, 523 F.3d 1323, 1340 (Fed. Cir. 2008) (“[M]eans-plus-function elements . . . are statutorily *limited* to the ‘corresponding structure, material, or acts described in the specification and equivalents thereof.’” (emphasis added) (citing 35 U.S.C. § 112, ¶ 6)).

And in this case, the ’092 Patent expressly states that reference numbers in the claims do not affect the scope of the claim. The specification states: “In the claims, *any reference signs placed in parentheses shall not be construed as limiting the claims.*” ’092 Patent, 7:39–41 (emphasis added). This statement strongly implies that the patentee did not intend for the reference numbers in the claims to carry any substantive weight.

The Court thus declines to rely exclusively on reference numbers in the claims to signal corresponding structure. And without resorting to the reference numbers, no structure is “clearly link[ed]” to the “output means” function. *Williamson*, 792 F.3d at 1352. As NXP’s briefing concedes, an “antenna” alone does not suffice to carry out the outputting function. Dkt. # 195 at 13 (“[A]n antenna alone, as proposed by Impinj, is not capable of outputting any data at all, and cannot perform the claimed function.”). Accordingly, the term is invalid for indefiniteness.

4. “output means (40, 34, 32) for outputting specific useful data (n×UDB), included in the data carrier circuit (28) after the specific useful data’s (n×UDB) storage, to the communication station”

Claim 19 of the ’092 Patent includes a term that is nearly identical to the term construed in claim 15 just above. Both are “output means” terms that attempt to designate structure by

1 relying on reference numbers. The only difference is that claim 19 contains a parenthetical with
2 three reference numbers—“(40, 34, 32)”—whereas the parenthetical in claim 15 also includes
3 reference number 25.

4 This term is indefinite for the same reasons discussed for the prior term. Because the
5 Court declines to rely exclusively on reference numbers in a claim to indicate corresponding
6 structure, this limitation is indefinite because the specification lacks corresponding structure.

7 B. The '097 Patent

- 8 1. “information-voltage generating means that are arranged to receive a control
9 signal . . . and that are arranged to generate the information voltage by using the
control signal”

10 The '097 Patent describes a data carrier that temporarily stores information capacitively.
11 '097 Patent, 1:1–18. The information is represented by the value of an “information voltage,”
12 which is produced by “information-voltage generating means.” *Id.* The specification describes
13 an embodiment in which the “information-voltage generating means” receive a “control signal”
14 and are further comprised of a charging-current generating stage, a voltage-raising means, and a
15 voltage-limiting means. *Id.* at 3:60–4:14.

16 Claim 1 of the '097 Patent exemplifies the patent's use of the term. Claim 1 describes a
17 data carrier containing “information-voltage generating means that are arranged to receive a
18 control signal . . . and that are arranged to generate the information voltage by using the control
19 signal.” *Id.* at 8:66–9:3. Claim 1 further states that the data carrier is “characterized in that the
20 information-voltage generating means have voltage-raising means that are arranged to raise the
21 voltage value of the control signal.” *Id.* at 9:3–5. The term “information-voltage generating
22 means” also appears in terms 3, 4, and 6.

23 The parties dispute the meaning of the term “information-voltage generating means that
24 are arranged to receive a control signal . . . and that are arranged to generate the information

1 voltage by using the control signal.” The parties first dispute whether this is a means-plus-
2 function claim subject to the strictures of § 112, ¶ 6. Impinj insists that this is a means-plus-
3 function limitation subject to § 112, ¶ 6. Dkt. # 135 at 18. NXP disagrees. Dkt. # 137 at 22.
4 Instead, NXP believes that the term should be given its plain and ordinary meaning, or “a circuit
5 that receives a control signal and generates the information voltage by using the control signal.”
6 *Id.* And if the Court construes the term to be a means-plus-function claim, the parties dispute
7 which structure(s) would qualify as corresponding structure.

8 The term uses the word “means.” There is thus a presumption that the claim contains a
9 means-plus-function limitation subject to § 112, ¶ 6. *Egenera*, 972 F.3d at 1372 (“We presume
10 that claim terms with the word ‘means’ invoke § 112(f).”). But the Federal Circuit has cautioned
11 that “the essential inquiry is not merely the presence or absence of the word ‘means.’”
12 *Williamson*, 792 F.3d at 1348. Rather, the “essential inquiry” is “whether the words of the claim
13 are understood by persons of ordinary skill in the art to have a sufficiently definite meaning as
14 the name for structure.” *Id.* (citation omitted). “To determine whether a claim recites sufficient
15 structure, it is sufficient if the claim term is used in common parlance or by persons of skill in the
16 pertinent art to designate structure, even if the term covers a broad class of structures and even if
17 the term identifies the structures by their function.” *Skky*, 859 F.3d at 1019 (citation and
18 quotation marks omitted).

19 The Court concludes that the “means” presumption has not been rebutted and that the
20 term is a means-plus-function limitation subject to § 112, ¶ 6. There is no evidence that the term
21 “information-voltage generating means” or similar terms are used “in common parlance or by
22 persons of skill in the pertinent art to designate structure.” *Id.* (citation and quotation marks
23 omitted). Nor is there any evidence that the term serves as “the name of a sufficiently definite
24 structure” or that “the words of the claim are understood by persons of ordinary skill in the art to

1 have a sufficiently definite meaning as the name for structure.” *Williamson*, 792 F.3d at 1349,
2 1351. Rather, the claim language is predominantly functional, not structural. The claim seeks to
3 capture virtually any means that can perform the function of “generat[ing] the information
4 voltage by using the control signal.” Impinj summarizes it well: “[T]he claim language does not
5 set forth what structure in the claimed circuit performs this function. It recites only what it
6 *does*.” Dkt. # 135 at 19. Thus, the claim is a means-plus-function claim subject to § 112, ¶ 6.

7 NXP responds that the claim term—read as a whole—conveys adequate structure. For
8 example, in addition to the claim’s functional language, claim 1 also describes the information-
9 voltage generating means as “receiv[ing] a control signal” and containing within it a “voltage-
10 raising means.” ‘097 Patent at 8:66–9:6. By describing the input and at least one element of the
11 information-voltage generating means, NXP says that the term is not governed by § 112, ¶ 6.

12 But this language does not convey adequate structure. To be sure, the language *partially*
13 describes the structure and operation of the information-voltage generating means; it explains
14 one element (voltage-raising means) and an input (a control signal). But a circuit component that
15 intakes a control voltage and applies “voltage-raising means” is insufficient to perform the
16 function of “generating” an “information voltage.” See *TriMed, Inc. v. Stryker Corp.*, 514 F.3d
17 1256, 1259 (Fed. Cir. 2008) (“If, in addition to the word ‘means’ and the functional language,
18 the claim recites sufficient structure for performing the described functions *in their entirety*, the
19 presumption of § 112 ¶ 6 is overcome” (emphasis added)); *Micro Chem., Inc. v. Great Plains*
20 *Chem. Co.*, 194 F.3d 1250, 1257 (Fed. Cir. 1999) (rebuttal of the means-based presumption
21 requires the claim to provide “sufficient structure . . . to perform the claimed function” (emphasis
22 added)); *MTD Products*, 933 at 1343 (noting that while a portion of the claim language discussed
23 structure, another portion was written in functional terms, which “tends to favor [the] position
24 that § 112, ¶ 6 applies”). Such a component would be nothing more than “voltage-raising

1 means.” If the “information-voltage generating means” were merely “voltage-raising means”
2 that receive a certain input, the patent would have no need to describe an “information-voltage
3 raising means” at all. And as described in the specification, other features are necessary to
4 generate an information voltage, including a charging-current generating stage and some form of
5 voltage-limiting means. ’097 Patent, 3:60–4:29. Even though the claim “describe[s] certain
6 inputs and outputs at a very high level,” the claim language does not sufficiently “inform the
7 structural character of the limitation-in-question or otherwise impart structure.” *Williamson*, 792
8 F.3d at 1351.

9 NXP also points to the declaration of its expert witness, Dr. Madisetti, to support its
10 conclusion that § 112 ¶ 6 does not apply. Dr. Madisetti opined that “this term is used in common
11 parlance by persons of skill in pertinent art to describe a structure (not a function) because
12 information-voltage generating structures were well known by POSITAs at the time of the
13 invention.” Dkt. # 137-4 at 38. He also opined that circuits that generate an information voltage
14 were “well-known in the art.” *Id.*

15 Dr. Madisetti’s declaration does not rebut the means-based presumption. First, Dr.
16 Madisetti’s declaration identifies the wrong inquiry. It is not enough that information-voltage
17 generating circuits were “well-known in the art” or that a POSITA could devise such a circuit.
18 *Cf. Blackboard*, 574 F.3d at 1384–85 (in the context of determining corresponding structure,
19 observing that it was not enough that “a person of skill in the art could devise some means to
20 carry out the recited function”). Rather, the relevant inquiry is whether the language in the claim
21 conveys sufficiently definite structure or otherwise serves as the name for structure or a class of
22 structures. *TriMed*, 514 F.3d at 1259–60 (“Sufficient structure exists when the claim language
23 specifies the exact structure that performs the functions in question.”); *Skky*, 859 F.3d at 1019;
24 *Williamson*, 792 F.3d at 1349–51.

1 Second, Dr. Madisetti’s statement that the “term is used in common parlance by persons
2 of skill in the pertinent art to describe a structure (not a function)” is conclusory. *See* Dkt. # 137-
3 4 at 38. Dr. Madisetti does not cite any evidence or provide any explanatory basis for his
4 conclusion. He does not, for example, point to other instances when the term was used by
5 members of the scientific community. Nor does he provide examples of the types of structures
6 that a POSITA would understand to be disclosed by the term. Because Dr. Madisetti provides no
7 basis or reasoning to support his opinion, the Court concludes that his conclusory statements do
8 not suffice to overcome the means-based presumption. *See Diebold Nixdorf, Inc. v. Int’l Trade*
9 *Comm’n*, 899 F.3d 1291, 1300–01 (Fed. Cir. 2018) (disregarding conclusory expert opinion that
10 a term would convey structure to a POSITA when the opinion was not supported by extrinsic or
11 other evidence); *cf. Intell. Sci. & Tech., Inc. v. Sony Elecs., Inc.*, 589 F.3d 1179, 1184 (Fed. Cir.
12 2009) (in the context of a summary judgment motion for infringement, concluding that
13 “[a]n expert’s unsupported conclusion on the ultimate issue of infringement will not alone create
14 a genuine issue of material fact”); *Telemac Cellular Corp. v. Topp Telecom, Inc.*, 247 F.3d 1316,
15 1329 (Fed. Cir. 2001) (“Broad conclusory statements offered by Telemac’s experts are not
16 evidence and are not sufficient to establish a genuine issue of material fact.”). As the Federal
17 Circuit explained in *Phillips*, “conclusory, unsupported assertions by experts as to the definition
18 of a claim term are not useful to a court.” 415 F.3d at 1318. There is no reason that this
19 principle should not apply equally to an expert’s unsupported statement that a POSITA would
20 understand the term in question to convey structure.⁸ Accordingly, the Court concludes that the
21 term “information-voltage generating means . . .” invokes § 112, ¶ 6.

23 ⁸ The Court notes, however, that Impinj did not submit its own expert declaration as to this patent
24 term. But because Dr. Madisetti’s declaration is conclusory, the Court does not believe that this is
dispositive.

1 The Court must next determine the claimed function of the term. *Williamson*, 792 F.3d at
2 1351. Impinj proposes the following function: “generat[ing] the information voltage by using
3 the control signal, by e.g. receiving a control signal CS of a voltage at most equal to the supply
4 voltage.” Dkt. # 135 at 18. NXP does not propose a function, but criticizes Impinj’s
5 construction for failing to adhere to the claim language. Dkt. # 150 at 13. While the Court does
6 not see any meaningful substantive difference between the claim language and Impinj’s proposed
7 function, the Court agrees that the function could be drafted in a manner that tracks the claim
8 language more closely and reads more naturally. Therefore, the Court determines that the
9 function is: “generating an information voltage by receiving and using a control signal that is of a
10 voltage value that is at most equal to the value of the supply voltage.” *See* ’097 Patent, 8:65–9:6
11 (using similar language in the patent claims).

12 Once a court has determined that § 112, ¶ 6 applies and determines the claimed function,
13 then “the court must determine what structure, if any, disclosed in the specification corresponds
14 to the claimed function.” *Williamson*, 792 F.3d at 1351. “Structure disclosed in the specification
15 qualifies as ‘corresponding structure’ if the intrinsic evidence clearly links or associates that
16 structure to the function recited in the claim.” *Id.* at 1352 (citation omitted).

17 Impinj proposes the following as corresponding structure: “‘a charging-current
18 generating stage 7 implemented in the form of an n-channel field effect transistor connected to
19 storage capacitor 5A, voltage raising means 8, and voltage-limiting means 9,’ as described in
20 Fig. 2 elements 6, 7, 8, and 9, 3:60–4:51.” Dkt. # 135 at 18. NXP rejects Impinj’s proposed
21 structure because it “improperly identifies structure that is unnecessary to perform the claimed
22 function.” Dkt. # 150 at 13. This, NXP says, is impermissible because a court may not
23 incorporate “structure from the written description beyond that necessary to perform the claimed
24

1 function.” *Micro Chem.*, 194 F.3d at 1258. Instead, NXP says that claims 1 and 4 expressly
2 limit the necessary structure to “voltage-raising means.”

3 The Court rejects NXP’s proposed structure and rejects Impinj’s proposed structure in
4 part. NXP’s proposed corresponding structure—“voltage-raising means”—does not suffice to
5 carry out the function of “generating” an “information voltage.” As discussed above, if an
6 information voltage could be generated using only “voltage-raising means,” then the term
7 “information-voltage generating means” would be superfluous; it would be nothing more than a
8 synonym for “voltage-raising means.” And as described in the specification, generating an
9 information voltage requires more than just a means for raising the voltage. ’097 Patent, 3:60–
10 4:29.

11 The Court agrees with portions of Impinj’s proposed structure. The Court agrees with
12 Impinj that the specification describes an “information-voltage generating means” with three
13 basic components: (1) a “charging-current generating stage,” (2) voltage-raising means, and (3)
14 voltage-limiting means.” *See* ’097 Patent, 3:60–4:29. But Impinj says that the “charging-current
15 generating stage” must be of a particular form: “an n-channel field effect transistor connected to
16 storage capacitor.” The Court rejects this as excessively narrow. The specification discloses the
17 corresponding structure of a “charging-current generating stage” before describing a particular
18 form that can “implement[]” that structure (the “n-channel field effect transistor”). Impinj has
19 not explained why the patentee is not entitled to the broader structure of “charging-current
20 generating stage.” This broader structure is clearly linked to the claim term. The specification
21 states that the information-voltage generating means “*have* a charging-current generation stage.”
22 *Id.* at 3:66–67 (emphasis added); *cf. Micro Chem.*, 194 F.3d at 1258 (“When multiple
23 embodiments in the specification correspond to the claimed function, proper application of
24 § 112, ¶ 6 generally reads the claim element to embrace each of those embodiments.”).

1 Accordingly, the Court determines that the corresponding structure is: “a charging-current
2 generating stage, voltage-raising means, and voltage-limiting means” and equivalents thereof.⁹

3 In conclusion, the Court construes this term to be a means-plus-function limitation
4 subject to § 112, ¶ 6. The Court concludes that the function is “generating an information
5 voltage by receiving and using a control signal that is of a voltage value that is at most equal to
6 the value of the supply voltage.” The Court concludes that the corresponding structure disclosed
7 in the specification is “a charging-current generating stage, voltage-raising means, and voltage-
8 limiting means” and equivalents thereof.

9 2. “voltage-raising means that are arranged to raise the voltage value of the control
10 signal”

11 According to the '097 Patent, the “information-voltage generating means” contain
12 “voltage-raising means.” The term “voltage-raising means” appears in independent claims 1 and
13 4, as well as dependent claims 2 and 5. For example, claim 1 describes “information-voltage
14 generating means . . . that are arranged to generate the information voltage by using the control
15 signal, characterized in that the information-voltage generating means have *voltage-raising*
16 *means* that are arranged to raise the voltage value of the control signal.” '097 Patent, 8:66–9:5
(emphasis added).

17 The parties dispute whether this is a means-plus-function term subject to § 112, ¶ 6.
18 Impinj says that the term is governed by § 112, ¶ 6. Dkt. # 135 at 21. NXP disagrees, instead
19 arguing that the term be given its plain and ordinary meaning, which it interprets to be “a circuit
20 that raises the voltage value of the control signal.” Dkt. # 137 at 24. And if the Court construes
21

23 ⁹ As explained in the following section, the Court concludes that the term “voltage-raising
24 means” as used in the '097 Patent refers to “a charge pump or the float-based structure described at 2:43–
48 of the '097 Patent.”

1 the term to be a means-plus-function limitation, the parties further dispute what structure(s) in
2 the specification qualify as corresponding structure.

3 The Court construes the “voltage-raising means . . .” term to be a means-plus-function
4 limitation subject to § 112, ¶ 6. The claim uses the word “means.” This creates a presumption
5 that the limitation is a means-plus-function claim subject to § 112, ¶ 6. *Egenera*, 972 F.3d at
6 1372. When patentees use the term “means,” courts presume that they did so deliberately. *See*
7 *Rodime PLC v. Seagate Tech., Inc.*, 174 F.3d 1294, 1302 (Fed. Cir. 1999) (“[T]his court has
8 presumed an applicant *advisedly* used the word ‘means’ to invoke the statutory mandates for
9 means-plus-function clauses.” (emphasis added)). This presumption has not been rebutted.

10 The claim language itself conveys no structure; it instead describes any device capable of
11 performing the function of “rais[ing] the voltage of the control signal.” While the Federal
12 Circuit has held that certain introductory phrases preceding the word “means” can help rebut the
13 presumption, *see, e.g., Enviro Corp. v. Clestra Cleanroom, Inc.*, 209 F.3d 1360, 1365 (Fed. Cir.
14 2000) (holding that “baffle means” adequately conveyed the structure of “baffle”), the
15 introductory language used here—“voltage-raising”—does not convey structure. Rather, the
16 introductory “voltage-raising” phrase is purely functional, merely repeating the function of
17 “rais[ing] the voltage” that comes after the word “means.” In other words, the claim does not
18 meaningfully differ from a claim that omits the introductory phrase “voltage-raising” and instead
19 reads: a “means . . . arranged to raise the voltage value of the control signal.” When rephrased in
20 this manner, it becomes clearer that the term is functional, not structural.

21 As with the prior term, NXP relies heavily on the declaration of its expert witness, Dr.
22 Madisetti, who opined:

23 A POSITA would understand that such a voltage-raising means are “a circuit that
24 raises the voltage value of the control signal.” Such circuits were well-known in
the art. For example, a POSITA would have understood that voltage values could

1 be raised using a charge pump or a voltage multiplier that was well known in the
2 art.

3 Dkt. # 137-4 at 41. But the Court finds Dr. Madisetti's opinion insufficient for the same reasons
4 identified for the "information-voltage raising means" term. First, a term does not convey
5 structure—and thus avoid § 112, ¶ 6—merely because a POSITA could think of some device that
6 can perform the voltage-raising function. The fact that a POSITA could identify two such
7 structures (a charge pump and a voltage multiplier) that can perform the claimed function does
8 not prove that the claim term is structural or serves as the name for a class of structures. *See*
9 *TriMed*, 514 F.3d at 1259–60 ("Sufficient structure exists when the claim language specifies the
10 exact structure that performs the functions in question."); *cf. Blackboard*, 574 F.3d at 1384–85
11 (in the context of determining corresponding structure, observing that it was not enough that "a
12 person of skill in the art could devise some means to carry out the recited function"). Second,
13 Dr. Madisetti provided no explanation or basis for his opinion, rendering it conclusory. *See*
14 *Diebold*, 899 F.3d at 1300–01 (disregarding conclusory expert opinion that a term would convey
15 structure to a POSITA when the opinion was not supported by extrinsic or other evidence).¹⁰

16 NXP also cites the Federal Circuit's decision in *Lighting Ballast Control LLC v. Philips*
17 *Electronics North Am. Corp.* 790 F.3d 1329 (Fed. Cir. 2015). In that case, the Federal Circuit
18 held that the district court did not clearly err when it concluded that the term "voltage source
19 means" was not a means-plus-function limitation. *Id.* at 1336, 1338–39. To be sure, that term
20 bears at least some facial similarity to the "voltage-raising means" term at issue here. But
21 *Lighting Ballast* is distinguishable on at least two grounds.

22
23 ¹⁰ The Court notes that Impinj did not submit its own expert declaration as to this term, either.
24 But as with the prior term, Dr. Madisetti's declaration is conclusory. Thus, the Court does not believe
that this is dispositive.

1 First, the court pointed to un rebutted testimony that the term referred to one particular
2 type of device: a “rectifier.” *Id.* at 1338–39. By contrast, Dr. Madisetti did not state that a
3 POSITA would understand the term to mean a particular, definite structure or class of structures.
4 Rather, he stated only that a POSITA might know of several different means of raising the
5 voltage, like a charge pump or voltage multiplier.¹¹ And there are apparently more means of
6 raising voltage: As NXP identifies, the specification explains that the voltage can be raised by
7 allowing the voltage source to “float in relation to a reference potential,” which allows the
8 control voltage to “be raised by a desired amount.” ’097 Patent, 2:44–48. The variety of means
9 by which to raise the control voltage distinguishes this term from the term in *Lighting Ballast*.

10 Second, the claim in *Lighting Ballast* contained additional language that conveyed
11 structure. The claim stated that the voltage source means “provid[ed] a constant or variable
12 magnitude DC voltage between the DC input terminals.” The court accepted the district court’s
13 conclusion that a POSITA would understand that a rectifier was the “only structure that would
14 provide ‘a constant or variable magnitude DC voltage.’” *Id.* at 1339. By contrast, there is no
15 additional claim language here that would convey structure to a POSITA.

16 Accordingly, the Court concludes that the “voltage-raising means . . .” limitation is a
17 means-plus-function term subject to § 112, ¶ 6. *Cf. Atmel* 198 F.3d at 1376 (neither party
18 appealing the district court’s conclusion that “high voltage generating means” is a means plus
19 function claim); *Lufthansa Technik AG v. Astronics Advanced Elec. Sys. Corp.*, No. C14-
20 1821RSM, 2016 WL 1626687, at *5 (W.D. Wash. Apr. 25, 2016) (noting that the parties agreed
21

22 ¹¹ At the *Markman* hearing, Impinj observed that the ‘951 Patent—another patent at issue in this
23 case—expressly equates a “charge pump” with a “voltage multiplier.” See ‘951 Patent, 1:15–17; Dkt.
24 # 244 at 116. As stated below, the Court determines that the corresponding structure includes a “charge
pump.” Therefore, to the extent that a voltage multiplier is equivalent to a charge pump, the Court’s
construction captures *all* of the structures identified by Dr. Madisetti.

1 that “means for supplying supply voltage . . . arranged for” was a means-plus-function term);
2 *Agere Sys., Inc. v. Broadcom Corp.*, No. CIV.A.03-3138, 2004 WL 1658530, at *30 (E.D. Pa.
3 July 20, 2004) (holding that “voltage clamping means” is a means-plus-function term).

4 Once a court has determined that § 112, ¶ 6 applies, it must then determine the claimed
5 function. *Williamson*, 792 F.3d at 1351. The claimed function is straightforward: “raising the
6 voltage value of the control signal.”

7 Finally, the court must identify the corresponding structure. *Id.* Impinj suggests the
8 following corresponding structure:

9 A charge pump which has: a charge-pump capacitor 11 connected between the
10 Supply Voltage V and the reference potential GND as a result of which the
11 voltage applied to the charge-pump capacitor 11 assumes the value of the supply
12 voltage V; a first switch 12 and a second switch 13 implemented in the form of
field effect transistors, arranged to switch over from their rest state to an active
state, as is indicated in FIG. 2 by broken lines if control signal CS is received,” as
described in Fig. 2, element 8, 4:30–48.

13 Dkt. # 135 at 17. NXP responds that Impinj’s proposed structure omits at least one other
14 corresponding structure. Dkt. ## 137 at 25; 150 at 14–15.

15 The Court agrees with NXP that Impinj’s proposed corresponding structure is too narrow.
16 First, the specification discloses a charge pump in general; it does not disclose only the specific
17 charge pump described by Impinj. The specification states, for example, that “it has . . . proved
18 particularly advantageous if the voltage raising means are implemented in the form of a charge
19 pump.” ’097 Patent, 2:48–50. This passage and others like it “clearly link[]” a particular
20 structure—a generic charge pump—to the voltage-raising function. *Williamson*, 792 F.3d at
21 1352 (citation omitted). Moreover, dependent claims 2 and 5 both describe voltage-raising
22 means that “are implemented *in the form of a charge pump* that is arranged to raise the voltage
23 value of the control signal by the value of the supply voltage.” ’097 Patent, at 9:8–10 (emphasis
24 added); 10:11–13. While this claim language does modestly limit the type of charge pump (by

1 stating that the charge pump must be “arranged to raise the voltage value of the control signal by
2 the value of the supply voltage”), Impinj has presented no reason to believe that the very
3 particular charge pump described in part of the specification is the only charge pump that can
4 complete the claimed function. Thus, it would be improper to interpret the independent claim as
5 limited to a particular form of charge pump when the dependent claims undeniably include
6 definite structure in the form of a generic charge pump. While an independent claim containing
7 a means-plus-function limitation need not be interpreted as *broader* than a dependent claim, *see*
8 *Medtronic, Inc. v. Advanced Cardiovascular Sys., Inc.*, 248 F.3d 1303, 1313 (Fed. Cir. 2001), it
9 would be odd to conclude that the independent claim is *narrower* than the dependent claim
10 containing definite structure. The presence of claims 2 and 5 clearly link the term “voltage-
11 raising means” to a generic charge pump that is capable of raising the control voltage.

12 Second, NXP points to a reference in the specification to another method for voltage
13 raising. The specification states:

14 In the case of the solutions according to the invention, provision may for example
15 be made for the voltage-raising means to be formed by a voltage source that can
16 be operated to float in relation to a reference potential of the circuit by which the
17 value of the control-signal voltage can be raised by a desired amount.

18 ’097 Patent at 2:43–48; Dkt. # 150 at 14–15. Impinj’s reply brief does not respond to NXP’s
19 argument. At the *Markman* hearing, Impinj’s counsel indicated that he was not sure that
20 Impinj’s proposed construction excluded this float-based method. Dkt. # 244 at 117–18. To
21 avoid any doubt, the Court agrees that this sentence clearly links or associates a float-based
22 structure to “voltage-raising means.” Accordingly, the Court construes the corresponding
23 structure as “a charge pump or the float-based structure described at 2:43–48 of the ’097 Patent.”

24 In conclusion, the Court construes this term to be a means-plus-function limitation
subject to § 112, ¶ 6. The Court concludes that the function is “raising the voltage value of the

1 control signal.” The Court concludes that the corresponding structure disclosed in the
2 specification is “a charge pump or the float-based structure described at 2:43–48 of the ’097
3 Patent” and equivalents thereof.

4 C. The ’951 Patent

- 5 1. “regulating an output voltage at the output of the input level regulator to a
6 continuous range of voltage levels”

7 The ’951 Patent describes a voltage multiplier circuit in which the voltage is “regulated.”
8 As described above, one such source of regulation in the voltage multiplier circuit is the “input
9 level regulator,” which receives an input from “a feedback bias control circuit” and outputs a
10 signal to the first of several voltage multiplier stages. ’951 Patent, 3:22–32. The parties dispute
11 the construction of claim language related to this input level regulator. In claim 1, for example,
12 the ’951 Patent states that “the feedback bias control circuit generates a feedback signal for
13 regulating an output of the input level regulator to a continuous range of voltage levels based on
14 a comparison between a voltage proportional to a voltage at the output of the second clocked
15 multiplier stage and a reference voltage.” *Id.* at 10:53–59. The parties dispute the meaning of
16 the term “regulating an output of the input level regulator to a continuous range of voltage
17 levels,” which also appears in similar form in claims 9 and 16.

18 NXP argues that no construction is necessary, and that the term should be given its plain
19 and ordinary meaning. Dkt. # 137 at 26. Impinj proposes a construction of “an output voltage
20 capable of being commanded to transition directly between any two distinct voltage levels in the
21 range.” Dkt. ## 135 at 24–25; 149 at 13–14. While the Court finds that construction is
22 necessary because the term could confuse a jury, the Court also rejects Impinj’s proposed
23 construction. The Court interprets a slightly broader term—“a feedback signal for regulating an
24 output of the input level regulator to a continuous range of voltage levels”—to mean “a feedback

1 signal that is fed into an input level regulator, which then uses the feedback signal to regulate a
2 voltage that varies within a continuous range of voltage levels.”

3 As described in the specification, a feedback bias control circuit produces a “feedback
4 bias control signal” or “feedback signal.” ’951 Patent, 4:17–28. This feedback signal is then fed
5 into other components of the voltage multiplier, such as the input level regulator. *See id.* at
6 3:22–32 (input level regulator having a “feedback bias control input”). Based on the feedback
7 signal, the input level regulator outputs a voltage value, which is then fed into the first voltage
8 stage. *Id.* at 3:29–32 (producing a “regulated voltage on signal line 34 in response to . . . the
9 feedback bias control signal”), 5:26–36. Importantly, the input level regulator does not merely
10 output a voltage to two discrete voltages, say 0V or 1V. Rather, the claims of the patent
11 emphasize that the input level regulator can output a voltage to a “continuous range” of voltages.
12 *See, e.g., id.* at 10:56.

13 Impinj seeks a construction that the output voltage of the input level regulator transitions
14 “between any two distinct voltage levels in the range.” Theoretically, it may be true that the
15 voltage transitions “between any two distinct voltage levels” in some embodiment of the
16 invention—it might change from .5V to .7V, for example. But such a construction draws
17 attention away from the inventive element of the ’951 Patent: the ability to produce a voltage
18 within a *continuous range* of voltages. Dkt. # 150 at 16. Impinj seems not to dispute that the
19 ’951 Patent can produce a voltage across a range of voltages. *See* Dkt. # 149 at 13 (“The parties
20 agree that the input level regulator of the recited voltage multiplier can regulate its own output
21 voltage across all voltage levels within a range.”). In light of that concession, it is unclear why
22 Impinj believes that the term should be construed to emphasize that the voltage transitions
23 “between two distinct voltages.” Impinj identifies nothing in the specification or claims to
24 support such a construction.

1 Impinj cites prosecution history to support its argument. *See Phillips*, 415 F.3d at 1317
2 (permitting consideration of prosecution history during claim construction). But the prosecution
3 history does not provide any support for Impinj’s use of the phrase “between two distinct
4 voltages.” During patent prosecution, the patentee distinguished the ’951 Patent from prior art
5 by observing that the prior art did not contain an input level regulator that regulates an output
6 voltage to a “range of voltage levels,” and that the first stage of the prior art enabled only a
7 binary output (i.e., “on” or “off”). Dkt. ## 136-3 at 7–8; 136-6 at 15. The patent examiner
8 agreed, stating that “‘regulating an output voltage . . . to a continuous range of voltage levels’ is
9 to be interpreted as an output voltage being regulated to a value within a continuous range of
10 voltage levels, wherein the continuous range of voltage levels comprises all voltage levels within
11 the end points of said continuous range.” Dkt. # 137-2 at 3. As NXP states, it was the *prior art*
12 that “was limited to two values, not the claimed invention, which explicitly applies to a range.”
13 Dkt. # 150 at 16. The patent prosecution makes clear that any construction of the term should
14 highlight the ability of the input level regulator to produce a range of different voltage outputs
15 and should not focus on “two distinct voltages.”

16 Impinj also argues that the output voltage is “commanded” to transition between various
17 voltages. NXP correctly observes in its briefing that the term “commanded” does not appear in
18 the ’951 Patent. Dkt. # 137 at 27. At the *Markman* hearing, Impinj agreed that the term
19 “commanded” is an awkward fit for this term, but that its construction sought to capture the
20 notion that the output voltage is determined by the feedback signal, which is an input to the input
21 level regulator. Dkt. # 244 at 128. It is not clear that NXP disputes this point (though it believes
22 that “commanded” is ambiguous). NXP stated at the *Markman* hearing that “there’s a feedback
23 circuit that sends . . . some signal back based on what the load is at the output that gets taken into
24 account by the input level regulator, which then outputs that 34 signal to the first stage.” *Id.* at

1 120; *see also* Dkt. # 150 at 16 (“the input level regulator has two inputs . . . which regulate the
2 voltage at the output of the input level regulator”).

3 To the extent that there is any disagreement, the Court recognizes Impinj’s general point.
4 The claim discusses a “feedback signal” that is used “for regulating an output voltage” of the
5 “input level regulator.” ’951 Patent, 10:54–56. In this context, the feedback signal “regulate[s]”
6 the output voltage by affecting it in some way. Based on this claim language, the input level
7 regulator receives a feedback signal and uses that feedback signal to help determine the voltage
8 (within a continuous range of voltages) to be outputted. The Court also agrees that the term
9 “commanded” makes for an awkward fit. The Court’s construction takes these considerations
10 into account by observing that the feedback signal “is fed into an input level regulator” and that
11 the input level regulator “uses the feedback signal to produce a voltage that varies within a
12 continuous range of voltage levels.”

13 Accordingly, the Court construes “a feedback signal for regulating an output of the input
14 level regulator to a continuous range of voltage levels” to mean “a feedback signal that is fed
15 into an input level regulator, which then uses the feedback signal to regulate a voltage that varies
16 within a continuous range of voltage levels.”

17 2. “regulated clock”

18 The parties dispute the meaning of the term “regulated clock” as used in claims 9 and 16
19 of the ’951 Patent. The claims describe a series of “regulated clock[s]” that are coupled to each
20 multiplier stage. ’951 Patent, 11:35–42, 12:32–35. In one embodiment, the regulated clock
21 operates in response to a clock input signal, a supply voltage, and the feedback signal produced
22 by the feedback bias control circuit. ’951 Patent, 5:26–35; Dkt. # 137 at 27–28.

23 NXP argues that no construction is necessary, and that the term should be given its plain
24 and ordinary meaning. Dkt. # 137 at 27. Impinj argues that the term should be construed as “a

1 clock signal with a signal amplitude determined by the feedback from the voltage multiplier
2 circuit.” Dkt. # 135 at 25. Because the Court is construing the noun “regulated clock,” the Court
3 interprets Impinj’s proposed construction as “*a clock that produces a signal with a signal*
4 *amplitude determined by the feedback from the voltage multiplier circuit.*” The Court construes
5 the term “regulated clock” to mean “a clock that is regulated based on feedback from the voltage
6 multiplier circuit.”

7 The term “regulated clock” must mean something more than a basic “clock.” This is
8 because “claim constructions [should ideally] give meaning to all of a claim’s terms.” *Apple,*
9 *Inc. v. Ameranth, Inc.*, 842 F.3d 1229, 1237 (Fed. Cir. 2016). The plain meaning of “regulated
10 clock” is a clock that is *regulated* in some manner. And for a clock to be regulated, something
11 must provide the regulation or do the regulating. Here, the thing that regulates (that is, adjusts or
12 controls) the output of the clock is feedback from other components in the voltage multiplier
13 circuit. For example, the specification describes one embodiment in which the “[r]egulated
14 clock circuit 20 operates in response to the clock (CLK), the supply voltage (VSUPPLY), and the
15 feedback bias control signal 58 for outputting the regulated clock output on signal line 38.” ’951
16 Patent, 5:32–34. The claims track the description. Claim 9, for example, describes a “first
17 regulated clock” in which the regulated clock is coupled with the feedback bias control circuit
18 “such that the feedback signal affects an output voltage of the first regulated clock.” *Id.* at
19 11:35–36, 11:52–55.

20 From the specification and claim language, it is clear that feedback from the voltage
21 multiplier circuit regulates the clock. This aligns with NXP’s presentation during the *Markman*
22 hearing in which counsel stated that “[r]egulated’ generally means there’s some sort of
23 feedback,” and that “when something’s regulated, it takes in feedback, and so what it’s
24 outputting is affected by that feedback.” Dkt. # 244 at 132. Importantly, however, the claims

1 describe slightly different arrangements for the regulated clock. Claim 9 states that the regulated
2 clock is coupled to the feedback bias control circuit, *see* '951 Patent, 11:53–54, while claim 16
3 lacks this limitation (which instead appears in dependent claim 17), *see id.* at 12:25–52.

4 Therefore, the Court's construction recognizes that the claims do not consistently identify the
5 source of the regulation; they instead suggest only that feedback from the voltage multiplier
6 circuit as a whole regulates the clock in some manner. A narrower construction would
7 impermissibly import the specification's limits into the claims and would ignore differences
8 between the claims. *See SciMed*, 242 F.3d at 1340; *Phillips*, 415 F.3d at 1314.

9 It is unclear whether NXP's "plain and ordinary meaning" construction captures this
10 concept. NXP says that the Court need not construe this term because claim 9 includes a
11 limitation in which the "feedback signal affects an output voltage of the [] regulated clock." Dkt.
12 # 150 at 17. But in its briefing, NXP says that a "regulated clock" is a "clock circuit that *may* be
13 regulated." Dkt. # 137 at 22–23 (emphasis added). A regulated clock *must* be regulated in some
14 manner. And claim 16—unlike claim 9—lacks specificity about how the clock is regulated or
15 the input source(s) of the clock. To avoid any ambiguity, the Court finds that construction is
16 necessary to emphasize that the clock must be regulated in some manner.

17 Impinj asks the Court to go further. It suggests that the proper construction of the term
18 "regulated clock" emphasizes that feedback from the voltage multiplier circuit affects the "signal
19 amplitude" of the clock's output. Dkt. # 135 at 26–27. Impinj's argument is based on
20 prosecution history. In its appeal brief to the U.S. Patent and Trademark Office, the patentee
21 attempted to distinguish the '951 Patent from a prior patent filed by Ragone et al. The patentee
22 stated that:

23 Ragone et al. does not show or suggest "wherein the feedback bias control circuit
24 is further coupled to the [first/second/third/fourth] regulated clock, such that the
feedback signal affects an output voltage of the [first/second/third/fourth]

1 regulated clock” Ragone et al. only switches the clock on and off to the
2 pump stages. Ragone et al. does not affect the magnitude of the clock as claimed
3 in claim 9.

4 Dkt. # 136-6 at 16 (quoting ’951 Patent, claim 9).

5 By distinguishing the Ragone et al. patent on the grounds that it does not “affect the
6 magnitude of the clock as claimed in claim 9,” Impinj asserts that the patentee limited the scope
7 of the term to forms of regulation that alter the clock’s “signal amplitude.”

8 A patentee’s statements during patent prosecution can serve as a disclaimer of the scope
9 of the patent, “precluding patentees from recapturing through claim interpretation specific
10 meanings disclaimed during prosecution.” *Omega Eng’g, Inc. v. Raytek Corp.*, 334 F.3d 1314,
11 1323 (Fed. Cir. 2003). But courts “decline[] to apply the doctrine of prosecution disclaimer
12 where the alleged disavowal of claim scope is ambiguous.” *Id.*; *see also Thorner*, 669 F.3d at
13 1366 (observing that “[t]he standard for disavowal of claim scope is . . . exacting”). “Absent a
14 clear disavowal in the specification or the prosecution history, the patentee is entitled to the full
15 scope of its claim language.” *Home Diagnostics*, 381 F.3d at 1358.

16 The prosecution history cited by Impinj does not rise to a “clear disavowal” of any clock
17 that does not affect the magnitude or amplitude of the output. First, it is unclear what feature of
18 the ’951 Patent was being distinguished from Ragone et al. The prosecution history states that
19 “Ragone et al. does not show or suggest ‘wherein the feedback bias control circuit is further
20 coupled to the first regulated clock, such that the feedback signal affects an output voltage of the
21 first regulated clock.’” Dkt. # 136-6 at 16 (quoting ’951 Patent, claim 9). From this, it is unclear
22 whether the patentee was trying to distinguish Ragone et al. from a generic “regulated clock,” or
23 whether the patentee was trying to say that Ragone et al. lacked a regulated clock *with a*
24 *feedback signal that affects the output of the regulated clock*. If the patentee intended to do only
the latter, examination of the claims reveals the flaw in Impinj’s interpretation. While claim 9

describes a regulated clock with an “output voltage” that is “affect[ed]” based on a feedback signal, claim 16 lacks such a limitation, describing instead a generic “regulated clock.” The limitation involving a feedback signal appears in claim 17, which is a dependent claim of claim 16. Thus, while claim 16 requires that the clock be regulated in some fashion, it does not necessarily require regulation of the output voltage magnitude. *See AK Steel*, 344 F.3d at 1242 (“[D]ependent claims are presumed to be of narrower scope than the independent claims from which they depend.”). The ambiguity of the prosecution history is exacerbated by the fact that the patentee only made this statement about claim 9; the statement does not involve claim 16 (which, again, contains different language than claim 9).

Second, it is at least plausible that the patentee only meant to suggest one possible manner by which the clocks in the ’951 Patent could be regulated, contrasting that method of regulation with the on/off clocks described by Ragone, et al. Because the prosecution history is ambiguous, the Court concludes that it does not amount to clear disavowal of patent scope. “When the alleged disclaimer is ambiguous or amenable to multiple reasonable interpretations, [the court] decline[s] to find prosecution disclaimer.” *Core Wireless Licensing S.A.R.L. v. LG Elecs., Inc.*, 880 F.3d 1356, 1367 (Fed. Cir. 2018).

Accordingly, the Court construes the term “regulated clock” to mean “a clock that is regulated based on feedback from the voltage multiplier circuit.”

3. “proportional”

The parties dispute the meaning of the term “proportional” as the term appears in at least claims 1, 9, and 16. An exemplary use of the term appears in claim 1 of the ’951 patent, which describes a “feedback bias control circuit” that “generates a feedback signal . . . based on a comparison between a voltage *proportional* to a voltage at the output of the second clocked multiplier stage and a reference voltage.” ’951 Patent, 10:53–59 (emphasis added). The

1 specification describes a feedback bias control circuit that produces a control signal that is based
2 on a comparison between (1) a voltage “proportional” to the output voltage (VOUT) that arrives
3 at the feedback bias control circuit from the last multiplier stage and (2) a reference voltage
4 (VREF). *Id.* at 8:9–13, 4:17–28, 6:1–20. This value informs the control signal, which is
5 outputted to other components of the circuit.

6 NXP argues that the term does not require construction and should be given its plain and
7 ordinary meaning. Dkt. # 137 at 28. Impinj’s position has shifted throughout the claim
8 construction process. Impinj initially proposed a construction of “a fixed or constant ratio to
9 another value.” Dkt. # 135 at 27. But in its briefing and at the *Markman* hearing, Impinj appears
10 to advocate for a construction that includes an element of time, such as is a ratio that is “fixed or
11 constant *over a period of time*.” Dkt. # 135 at 27 (emphasis added); *see also* Dkt. # 244 at 140.
12 And in Impinj’s reply brief, Impinj stated that it *agrees* with NXP that “the ratio can be adjusted
13 at different times,” further confusing its position. Dkt. # 149 at 15. The Court construes the term
14 “proportional” to mean “a constant ratio to another value that can be adjusted at different times.”

15 “Proportional” is a common word that carries a common definition: “having the same or
16 a constant ratio.” “Proportional,” *Merriam-Webster Dictionary*, [https://www.merriam-](https://www.merriam-webster.com/dictionary/proportional)
17 [webster.com/dictionary/proportional](https://www.merriam-webster.com/dictionary/proportional); *see also* “proportional,” *Cambridge Dictionary*,
18 <https://dictionary.cambridge.org/us/dictionary/english/proportional> (“If two amounts are
19 proportional, they change at the same rate so that the relationship between them does not
20 change.”). The key to this common-sense understanding is that the relationship between the two
21 variables is linear and constant. That ratio could be 1:1, 2:1, or 10:1. But the word connotes a
22 linear relationship between two variables.

23 At the *Markman* hearing, however, NXP clarified that its “plain and ordinary meaning”
24 construction is *not* limited to a linear relationship between variables. Dkt. # 244 at 137. Rather,

1 NXP believes the term “proportional” captures, for example, logarithmic relationships and
2 exponential relationships, too. *Id.* Such a construction defies the common usage of the word
3 “proportional.” Nothing in the patent suggests deviation from this common definition of the
4 term. Moreover, NXP did not make this argument in its briefing, nor did it provide any source
5 (like a dictionary) suggesting that the ordinary meaning of “proportional” covers such concepts.
6 Thus, the first part of the Court’s construction—“a constant ratio to another value”—captures the
7 common definition of the word “proportional.”

8 The second portion of the Court’s construction—that the ratio “can be adjusted at
9 different times”—is necessary in light of the particular nature of the ’951 Patent. While
10 “proportionality” conveys a constant or linear ratio—say, 2:1 or 5:1—a proportion can change
11 over time. And indeed, the ’951 Patent allows the ratio to change. In one embodiment, for
12 example, the voltage is modified to be “proportional” to the output voltage by sending the output
13 voltage through a series of resistor dividers. ’951 Patent, 4:17–28, 6:1–20. Depending on
14 whether those resistor dividers are opened or closed, the ratio between the output voltage and the
15 “proportional” voltage can change. The Court’s construction conveys this feature of the ’951
16 Patent.¹²

17 NXP argues that Impinj’s construction ignores language in the patent that allows for the
18 voltage to “dynamically change.” The patent describes a feedback bias control circuit that
19 includes a “switched resistor-divider” or a “switched capacitor-divider” that can be “configured
20 to *dynamically change* a value of the voltage proportional to the voltage at the output of the
21 second clocked multiplier stage.” *Id.* at 9:9–20 (emphasis added); *see also id.* at 8:31–38. The

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23 ¹² Because the patent describes an invention in which the ratio changes over time, the Court
24 rejects Impinj’s modified construction that proportionality refers to a “fixed ratio *over time*.” Dkt. # 149
at 15. Such a construction would confuse a jury by implying that the ratio is permanent or semi-
permanent, when in fact the ratio can change.

1 patent discusses this feature in dependent claims 6 and 12. *See id.* at 11:15–17 (“dynamically
2 change a value of the voltage proportional to the voltage at the output”), 12:11–14 (same). The
3 Court’s construction incorporates this feature of the patent by allowing the ratio to change or be
4 adjusted over time. It seems that Impinj agrees: its brief concedes that the “proportional” ratio
5 “can be adjusted at different times.” Dkt. # 149 at 15; *see also* Dkt. # 136-2 at 22 (Impinj’s
6 expert stating that the “constant ratio based on the output voltage that can be switched based on
7 the resistive divider network”). To be sure, “dynamically” suggests that the ratio can change
8 extremely quickly. But nothing in the Court’s construction should be read to exclude an
9 embodiment in which the ratio changes quickly or even constantly.

10 Accordingly, the Court construes the term “proportional” to mean “a constant ratio to
11 another value that can be adjusted at different times.”

12 V.

13 CONCLUSION

14 The Court construes the disputed claim terms as described in this order.

15 Dated this 4th day of November, 2022.

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17 John H. Chun
18 United States District Judge
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